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ASIC Technology Symposium 2009 Invitation

KAL Katav Associates Silicon Technologies Ltd, is pleased to invite you to our Technology Symposium, which will take place on Tuesday, January 20th 2009, at the **Israel Air Force Center**, 15 Jabotinski st. Hertzelia . Free of charge with advanced registration.

Agenda:

08:30-09:00 Registration and Reception

09:00-09:30 Welcome, by Mr. Adi Katav- General Manager

09:30-11:00 "Debugging & Verification using visualization technologies", by Mr.Pascal Bolzhauser- senior applications eng. of Concept Engineering

11:00-12:30 "Analog high Voltage ASIC design techniques", by Prof. Otto Manck, General Manager of MAZ.

12:30-13:00 Lunch

13:00-14:30 "A new vision of BiCMOS - The Modus U6BC Process", by Dr. Joachim Scheerer, Co-CEO of Prema Semiconductors.

14:30-15:00 "Easy Migration from Single-core to Many-core processor design" by Peleg Avialy, VP R&D of Plurality (KAL customer)

15:00-15:30 Prize announcement

On line registration: http://www.kaltech.co.il/KALSymposium.php
Email registration: kal@kaltech.co.il/KALSymposium.php

Please fill out your details at the fax form below and fax it to: 04-6201328

Name:	Company	Title
Phone	Mobile	

KAL Katav Associates Silicon Technologies LTD.



About KAL:

KAL is a leading agencies company focused on ASIC services for the Middle East markets. KAL has world wide partners to provide the customer with an ASIC services that meet quality, schedule in competitive price. KAL is the first to offer Project Management Service as a stand alone service. KAL is proving the customers with feasibility study service, ASIC Project Management service as well as Supply Chain Service, Design service and Turnkey service for ASIC project. Our strong capabilities and wide experience drive from closely working with a large variety of clients within the Defense, Industrial, Medical, Communication, Consumer and Security applications. KAL delivers its customers with the ultimate "Total Solution" approach. www.KALtech.co.il

About Concept Engineering:

Concept Engineering is a privately held company based in Freiburg, Germany. It was, founded in 1990 to develop and market innovative schematic generation and viewing technology for use with logic synthesis, verification, test automation and physical design tools. The company's customers are primarily original equipment EDA tool manufacturers (OEMs), in-house CAD tool developers, semiconductor companies, IC/ASIC designers and FPGA designers. www.concept.de

About MAZ Brandenburg:

MAZ Brandenburg GmbH is a well established ASIC design house established in 1993. MAZ is privately owned and focused on ASIC design for High voltage, analog, mixed signal as well as mixed signal SOC (with processor such as ARM). MAZ is located in Berlin, Germany and provide turnkey ASIC solution as well as design project for consumer, industrial and automotive projects. www.maz-br.de

About Prema Semiconductor:

PREMA Semiconductor is a leading ASIC manufacturer for a complete range of analog and mixed-signal ICs; from below 1V up to 100 V supply (analog and digital); from circuit development, chip layout to prototyping and series production; from simple chips to complex systems-on-chip solutions (SoC). PREMA Semiconductor provides advanced, technologically leading bipolar and BiCMOS semiconductor technologies for Analog and Mixed-Signal Large Scale Integration (ALSI), for midsize to high volume projects. We offer rapid prototype turnarounds for short time-to-market periods. www.prema.com







